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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	Art Unit:	2824
Ebrahim Andideh et al.	§	Examiner:	Jung H. Hur
Serial No.: 10/712,205	§	Atty Docket:	ITL.1008US
Filed: November 12, 2003	§	P15533	
For: Parallel Electrode Memory	§	Assignee:	Intel Corporation
	§		
	§		

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed November 7, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: November 28, 2006
I hereby certify that this document is being facsimile transmitted to the
United States Patent and Trademark Office (Fax No. 571/273-8300) on the
date indicated above.
Cynthia L. Hayden
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